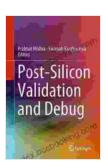
Unlock the Power of Post-Silicon Validation and Debug with Prabhat Mishra's Essential Guide

In the realm of electronic system design, post-silicon validation and debug play a pivotal role in ensuring the reliability and functionality of complex integrated circuits (ICs). Prabhat Mishra, an industry expert with decades of experience in design verification, has authored an indispensable book that provides a comprehensive roadmap to navigate the challenges of post-silicon validation and debug.



Post-Silicon Validation and Debug by Prabhat Mishra

★ ★ ★ ★ 5 out of 5

Language : English

File size : 22638 KB

Text-to-Speech : Enabled

Screen Reader : Supported

Enhanced typesetting : Enabled

Print length : 649 pages



Post Silicon Validation and Debug unravels the intricate details of this critical phase in the IC development cycle. Readers embark on a journey that begins with an overview of post-silicon validation, establishing a solid foundation for understanding the concepts and techniques involved.

Unveiling the Secrets of SoC Validation

Mishra delves into the intricacies of SoC (System-on-Chip) validation, providing an in-depth analysis of the challenges and solutions encountered when verifying complex SoCs. The book covers:

- Testbench architectures for SoC validation
- Verification of memory controllers and cache coherence
- Dealing with signal integrity issues
- Power and performance analysis

Mastering Functional Verification

Functional verification is a cornerstone of post-silicon validation. Mishra provides a thorough examination of this aspect, guiding readers through:

- Verification methodologies and coverage metrics
- Functional coverage analysis and simulation
- Emulation and prototyping techniques
- Assertion-based verification

Troubleshooting and Bug Resolution

Inevitably, post-silicon validation and debug involve troubleshooting and resolving bugs. Mishra empowers readers with practical techniques for:

- Identifying and analyzing failure mechanisms
- Using advanced debug tools and techniques
- Performing root cause analysis

Developing and implementing corrective actions

Real-World Case Studies and Industry Insights

Post Silicon Validation and Debug is enriched with real-world case studies and industry insights, providing valuable lessons learned from practical experiences. Mishra shares his knowledge of:

- Common pitfalls in post-silicon validation
- Best practices for successful debugging
- Emerging trends in validation and debug technology
- The latest industry standards and regulations

A Comprehensive and Essential Resource

Prabhat Mishra's *Post Silicon Validation and Debug* is an indispensable resource for:

- Design verification and validation engineers
- IC designers and architects
- Project managers responsible for SoC development
- Students and researchers in the field of digital design
- Anyone involved in the design and verification of electronic systems

By equipping readers with the knowledge and skills to navigate the complexities of post-silicon validation and debug, this book empowers them to deliver high-quality ICs with confidence. Free Download your copy today and unlock the potential for seamless product development.

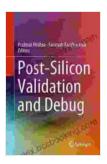
About the Author

Prabhat Mishra is a seasoned design verification expert with over 30 years of experience in the semiconductor industry. He has held leadership positions at leading technology companies and is widely recognized for his contributions to the field of digital design verification. Mishra is a prolific author and sought-after speaker, sharing his knowledge and insights with the global electronics community.

Free Download Your Copy Now!

Don't miss the opportunity to acquire this invaluable resource. Free Download your copy of *Post Silicon Validation and Debug* today and elevate your skills in the crucial field of post-silicon validation and debug.

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